

## Electronic Acknowledgement Receipt

EFS ID:	1052501
Application Number:	10595303
Confirmation Number:	7114
Title of Invention:	Method and Device For Secure, Insulated and Electrically Conductive Assembling of Treated Semiconductor Wafers
First Named Inventor:	Roy KNETCHTEL
Customer Number:	21967
Filer:	Rodger L. Tate/Mildred Ayim
Filer Authorized By:	Rodger L. Tate
Attorney Docket Number:	60291.000048
Receipt Date:	19-MAY-2006
Filing Date:	
Time Stamp:	16:10:39
Application Type:	U.S. National Stage under 35 USC 371
International Application Number:	

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Transmittal letter	DOC011.PDF	87678	no	1

<b>Warnings:</b>					
<b>Information:</b>					
2	Translation of Foreign Priority Documents	DOC014.PDF	1838686	no	16
<b>Warnings:</b>					
<b>Information:</b>					
Total Files Size (in bytes):				1926364	
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><b>New Applications Under 35 U.S.C. 111.</b> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><b>National Stage of an International Application under 35 U.S.C. 371</b> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>					